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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s) Bapst et al.
Docket No.: YOR920030633US1
Serial No.: 10/775,854
Filing Date: February 10, 2004
Group: 2883
Examiner: Jerry M. Blevins

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *Bobbett Blake* Date: May 18, 2006

Title: Circuit Board Integrated Optical Coupling Elements

TRANSMITTAL LETTER

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith is a Response After Final Rejection relating to the above-identified patent application. There is no additional claim fee due in connection with the Response.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **IBM Corporation Deposit Account No. 50-0510** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

Kevin M. Mason

Dated: May 18, 2006

Kevin M. Mason
Attorney for Applicant(s)
Reg. No. 36,597
Ryan, Mason & Lewis, LLP
1300 Post Road, Suite 205
Fairfield, CT 06824
(203) 255-6560



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RESPONSE AFTER FINAL REJECTION

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Sir:

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In response to the outstanding final Office Action, dated March 22, 2006,
Applicants submit the following remarks.